

BYTE NYBBLES

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A TMS-9900 Monitor

F000 MONITOR
 F002 BOOT

Everyone has their own idea of what a good monitor should and should not do. The TMS-9900 monitor described here is aimed at a small system (without disks) with a terminal (64 by 32 screen size) for I/O (input/output). It has been designed such that programs (which may be cross-assembled elsewhere) can be debugged efficiently. To this end, the monitor contains an instant assembler, a disassembler and comprehensive user program tracing facilities. The instant assembler allows modifications in code to be made quickly, since calculating op codes is difficult because the op code fields are not aligned on nybble boundaries.

The monitor occupies slightly less than 256 bytes of memory and has been assembled to occupy hexadecimal locations F400 thru FFFE. Three workspaces are required in programmable memory and these are placed just below the monitor at F3E0, F3C0, and F3A0, respectively. A stack is required, and this starts just below the three workspaces. The stack is used for subroutine linkage and may be used by all user programs. The monitor also makes use of interrupt level 1 (for tracing) and the XOP vectors 0 to 5 inclusive (See figure 1). The description of the monitor is divided into two parts: how to use the monitor, and a general description of how the monitor has been implemented.

How to Use the Monitor

When the monitor is started (by reset or load signals), it outputs << TMS9900 MONITOR >> to the terminal followed by the prompt >. The monitor is now ready to accept commands, all of which have a similar format:

COMMAND :: = { <HEXNO> } <COMMAND LETTER>

The optional hexadecimal number (only the last four digits count), unless otherwise stated, is taken to be a memory address. If no hexadecimal number has been input, then a default action takes place. The monitor keeps and

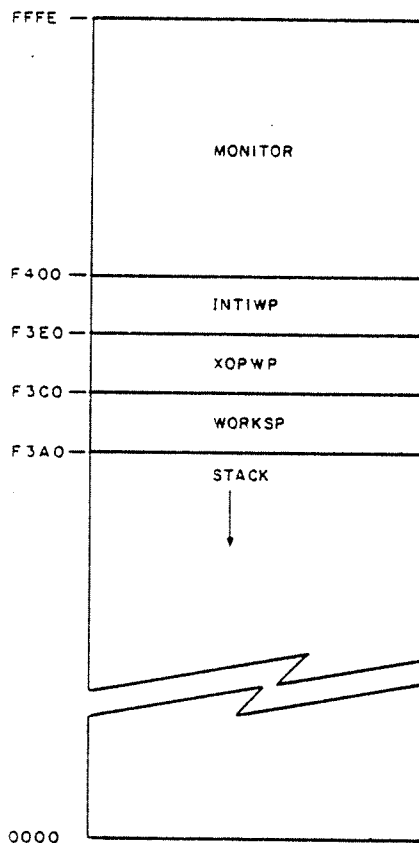


Figure 1: Memory usage in the Texas Instruments TMS-9900 system that uses the authors' monitor. The monitor occupies slightly less than 256 bytes of memory. Three workspaces are required, and they are placed just below the monitor. The stack, used for subroutine linkage, starts immediately below the three workspaces. The monitor also makes use of interrupt level 1 (for tracing) and the XOP vectors 0 to 5, inclusive.

updates two variables, low and last opened cell. The default action takes its address (hexadecimal number) from one of these. In all the examples that follow, the characters that the user inputs are underlined.

Command L

This command allows the monitor variable low to be set. The default action is to output the current value of low, else it is set to the hexadecimal number input. No other command changes the value of low, eg:

```
>2000L
>L
  LOW = 2000
>
```

Command

This command (space) allows memory cells (locations) to be examined and changed. The default action is to open the last opened cell. It outputs the address of the opened cell and its current contents. To change the contents, input a hexadecimal number followed by a delimiter; otherwise, input a delimiter only. If a space is used as a delimiter, then the next cell is opened, a minus sign opens the previous cell, and any other character will return control to the monitor. This command updates the monitor variable last opened cell, eg:

```
>2000  
  2000 1234  
  2002 6789 05678  
  2004 9ABC  
  2002 5678 <CR>
```

The symbol <CR> indicates a carriage return and is used throughout this article.

```
>C6P
00C6      2CE0      XOP      @ =011P,3
00CA      2CE0      XOP      @ =0108,3
00CE      2CE0      XOP      @ =011D,3
00D2      06A0      BL       @ =022E
00D6      0204      LI       R4, =0013
00DA      9064      CB       @ =0182(R4),R1
00DE      1305      JEQ      =00EA
00E0      0604      DEC      R4
00E2      18FB      JOC      =00DA
00E4      2CE0      XOP      @ =0126,3
00E8      10F2      JMP      =00CE
00EA      0A14      SLA      R4,1
00EC      C124      MOV      @ =0196(R4),R4
00F0      C0C3      MOV      R3,R3
00F2      0454      B        *R4
00F4      7FA0      SB       @ =0080,*R14+ <CR>
>
```

Listing 1: An example of the use of command P, a command in the authors' TMS-9900 monitor that lists the memory contents in disassembled form starting at the input address (in this case, hexadecimal C6). 16 disassembled instructions are listed. Inputting a space causes one more disassembled instruction to be output each time.

Command /

This command allows ASCII characters to be written into consecutive memory locations starting at the input address. The default action is to start at the last opened cell. Only characters greater than ASCII hexadecimal 20 (space) and also ASCII hexadecimal 00 may be input. Any other character (except ASCII hexadecimal 08 (backspace)) returns control to the monitor. The use of backspace allows a limited editing facility on the current line. This command updates the monitor variable last opened cell. Note that if ASCII 00 (control shift @) is input, then underscore is output.

```
>5010/
  5010 4142 /123456789ABCDEF0/
  5020 0000 /12 <CR>
>5010 
  5010 3031 <CR>
>
```

Command P

This command lists memory contents in disassembled form starting at the input address. The default action is to start at address low. It lists 16 disassembled instructions, after which inputting further spaces will list one more disassembled instruction; any other character passes control back to the monitor (See listing 1).

Command .

This command enters the instant assembler mode. The default action is to open the last opened cell. The address of the opened cell, its current contents and a . are output. The assembly mnemonic is input *followed by a space*. The mnemonic is checked to see if it is legal; if not, then ?? is output. The operands must then be input using the same format as the disassembler output, using R to denote registers, etc. The instant assembler will prompt if it can, and control is passed back to the monitor if any character less than ASCII hexadecimal 20 is input. Any other illegal characters simply cause ?? to be output and the user is allowed to try again. If no address is input for a jump address, a displacement of 00 is used. This command updates the monitor variable last opened cell, eg:

```
>7000.
  7000      4CA0      .MOVA        ??
  7000      4CA0      .MOVB        @ =1234(R10)*R15-  
  7004      7FE0      .LI        R1 =3<CR>
>7000 
  7000      DFEA        (Check code produced)
  7002      1234  
  7004      0201  
  7006      0003 <CR>
>
```

Command O

This command outputs the contents of memory in block form starting at the input address. The default action is to start at address low. It will output 64 locations after which inputting further spaces will output further 32 word

```

>C6L
>O
00C6 2CE0 011D 2CE0 0108 2CE0
00D0 011D 06A0 022E 0204 0013 9064 0182 1305
00E0 0604 18FB 2CE0 0126 10F2 0A14 C124 0196
00F0 C0C3 0454 7FA0 0080 7FE0 070E 01C6 01E8
0100 01EE 0200 0210 0222 3E20 544D 5339 3930
0110 3020 4D4F 4E49 544F 5220 3C3C 000D 0A0A
0120 2020 2020 3E00 203F 3F00 0A0D 0A20 2020
0130 2020 0020 5354 4143 4820 4F56 4552 464C<CR>
>

```

Listing 2: An example of the use of command O, which outputs the contents of memory in block form starting at the input address. 64 locations are output.

blocks. Inputting any other character passes control back to the monitor (See listing 2).

Command Z

This command searches memory sequentially for occurrences of the hexadecimal number input. The search begins at address low. If the hexadecimal number is found, then the address and contents are output. To change the hexadecimal contents input a hexadecimal number followed by a delimiter; otherwise input a delimiter only. If a space is used as a delimiter, then searching continues; other characters pass control back to the monitor. The Z routine will search all of memory (64 K bytes) and if the search is unsuccessful, then NOT FOUND is output.

```

>1234Z
7000 1234 0 (Change Contents)
7003 1234
7F9A 1234
NOT FOUND
>

```

Command M

This command allows a block of data to be moved from one part of memory to another. The instruction syntax is as follows:

<START ADDRESS> : <FINISH ADDRESS> <DESTINATION ADDRESS>

The software checks that the start address is less than the finish address to stop wrap around. After the three parameters have been input, the question YES or NO? is output. It must be answered with the letter Y if the move is to take place, eg:

```

>MOVE BYTES 100:200 TO A000
YES OR NO? Y
>

```

Command ↑Z (Control Z)

This command clears the terminal screen (in our implementation) and branches to the start of the monitor.

Command G

This command causes execution to pass to the input address. The default action is to branch to the start of the monitor.

Command W

This command allows the user workspace to be set. It is intended to set a user program's workspace when the tracing facilities are used. The default action is to output the value of the current user workspace.

```

>1000W
>W
WP = 1000
>

```

Instruction R

This instruction prints the contents of the registers taking the input address as the workspace pointer. The default action uses the user workspace pointer.

```

>1000 R
WP = 1000
R0 = 0400 R1 = FFFF
R2 = 8608 R3 = 370A
R4 = 0000 R5 = 0012
R6 = 0080 R7 = 865E
R8 = 0012 R9 = 0002
R10 = 0002 R11 = 830C
R12 = 0000 R13 = 0001
R14 = 4006 R15 = 0001
>

```

Command H

This command allows up to four breakpoints to be set in user programs. If during the T (Trace) or X (execute) instructions a breakpoint instruction is executed, control is passed to the monitor. The registers and environment of the user program may be examined and altered using any of the other monitor commands. The user program may be restarted from the breakpoint with further X, T, or S (Step) commands.

The H command allows the four breakpoints to be set in a user program. On receipt of the H command, BP1 SET AT XXXX is output. Inputting a hexadecimal number will change the address of the breakpointed instruction. Using a space as a delimiter will proceed to the next breakpoint; any other character will pass control to the monitor.

```

>H
BP 1 SET AT 124C
BP 2 SET AT 124E 1230 (Change Breakpoint 2)
BP 3 SET AT 1260<CR>
>

```


Command T

This command allows a user program to be traced from the start address supplied. The default action is to continue the user program from where it was before. The instruction outputs the addresses of the next 32 instructions that the computer executes, followed by a ?. Inputting further spaces will trace further sets of 32 instructions; any other character passes control to the monitor. If a breakpointed instruction is executed, then control passes back to the monitor. Note: If the traced program outputs to the terminal, these characters will be mixed with the instruction addresses.

Command X

This command is similar to the T command, except that the instruction addresses are not output. Control is passed back to the monitor only if a breakpointed instruction is executed. It is possible to execute the monitor itself using the X command, except of course that the monitor program runs much more slowly.

Command S

This command allows a user program to be single-stepped from the input address. The default action is to continue where it was before. The software outputs a disassembly of the instruction that has just been executed and the contents of the status register.

Monitor Implementation

Firmly believing in structured programming, we felt it necessary to be able to nest subroutine calls to an unlimited depth. This is not possible with the BL (branch and link) instruction since it always puts the return address in R11 of the current workspace. This means that R11 must be saved elsewhere before further branch and link instructions are used.

The BLWP (branch and link workspace pointer) instruction allows unlimited nesting if a predefined workspace is used for each subroutine (gives nonreentrant code). Obtaining a completely new workspace at each subroutine call can be very wasteful of memory space and furthermore, many variables usually have to be accessed via the old workspace. Clearly this results in a situation where R15, R14 and R13 cannot be used (since they contain linkage data), and variables have to be accessed using indirect addressing, yielding too many unnecessary multiple-word instructions.

To overcome these limitations, we used the TMS-9900 XOP (extended operation) instruction to augment the

instruction set. XOP0, XOP1, XOP4 and XOP5 have been coded to allow a good clean subroutine linkage that greatly outweighs the added execution time to perform an XOP instruction. All the XOP routines share the same workspace (XOPWP) and, consequently, interrupts are not allowed during their execution. The four routines use a stack that is pointed to by R10 of the XOPWP.

For subroutine calls, XOP0 is used. For example, to jump to subroutine REGISTER, the instruction XOP @ REGISTER, 0 would be used. The return address (address of the next instruction) is placed on the stack and the stack pointer is decremented. The next instruction executed is at address REGISTER. No register of the current workspace or the status is altered.

For subroutine return, XOP1 is used. For example, to return from the subroutine REGISTER, the instruction XOP R0, 1 would be used. R0 is a dummy parameter. The return address is obtained from the top of the stack and the stack pointer is incremented.

XOP4 and XOP5 are used to save and restore the workspace registers, respectively. For example, XOP RX, 4 pushes copies of registers 0 thru RX onto the stack while XOP RX,5 restores registers 0 thru RX by popping them off the stack and putting them in the current workspace. Register 9 of the XOPWP is used as a stack limit, as a precaution against the stack growing too far.

XOP2 and XOP3 are used for input and output to the terminal, respectively. XOP2 inputs a character from the terminal keyboard and places it at the specified location. For example, XOP RX,2 inputs one character and stores it in RX. XOP @ BUFFER,2 inputs one character and stores it at memory address BUFFER. XOP3 is really a print string operation in that it outputs characters from the specified address until a 00 byte has been output (the terminal is assumed to ignore the NUL code), eg:

LI	R0,	'AD'	
LI	R1,	'D'	
XOP	R1,	3	Prints "D"
XOP	R0,	3	Prints "ADD"

The monitor itself is quite straightforward, and a brief description should suffice. The first routine is an initialization routine, which clears the interrupt mask and initializes the interrupts and XOP vectors, low, last opened cell, stack pointer and the stack limit.

The next section is the monitor kernel. It recognizes the input command and jumps to the appropriate routine. The instructions command letters are in table INTAB, and the routine addresses are in SUBTAB. There is space for four more commands which we envisage will be used for cassette or disk drivers.

In this section, an assembler command WREN is used. This stands for write enable. The first 2 K bytes of memory is write protected. This prevents programs which are under development from running amok and overwriting themselves. This protection is implemented by external hardware, which simply inhibits the write enable signal to the memory. After a write enable instruction, only the next instruction is able to write into the protected memory.

An Example of the Use of the Tracing Facility in the Authors' Monitor.

1. The program is entered into memory using the instant assembler.
2. The user workspace is assigned.
3. A breakpoint is set on the last instruction.
4. The program is traced from the beginning.
5. Single step (from where trace ended).
6. Registers are examined (only R0 and R1 are in use).
7. The program is single stepped again.
8. The program is executed until the breakpointed instruction has been executed.
9. The final value of R0 is displayed.

```

>A000.
A000 FFFF .CLR L R0 L
A002 FFFF .LI L R1, =20 L
A006 FFFF .A L R1, R0 L
A008 FFFF .DEC L R1 L
A00A FFFF .JOC L =A006 L
A00C FFFF .SRL L R0, 1 L
A00E FFFF .AI L R0, =200 <CR>
>1020W
>H
BP 1 SET AT 0000 A00E <CR>
>A000T
A000 =>A002 =>A006 =>A008 =>A00A =>A006 =>A008 =>A00A =>
A006 =>A008 =>A00A =>A006 =>A008 =>A00A =>A006 =>A008 =>
A00A =>A006 =>A008 =>A00A =>A006 =>A008 =>A00A =>A006 =>
A008 =>A00A =>A006 =>A008 =>A00A =>A006 =>A008 =>A00A => ? <CR>
>S
A006 A001 A R1, R0 ST = C001
>R
WP = 1020
R0 = 0129 R1 = 0016
R2 = 9000 R3 = 0200
R4 = 3000 R5 = 0200
R6 = 910E R7 = 9106
R8 = 000D R9 = 0006
R10 = 9000 R11 = 82E2
R12 = 0000 R13 = 1040
R14 = 85EA R15 = C001
>S
A008 0601 DEC R1
>X
BP AT A00E WP = 1020 ST = C001
>1020 L
1020 0308 <CR>

```

This simple feature has proved to be very useful and it is a rare occasion indeed when a program is completely destroyed (See figure 2).

Perhaps a brief description of the instant assembler and disassembler would be of interest. The starting point is to understand the op code structure (See table 2). It can be seen that the instruction set can be divided into nine basic types and a subroutine can be written to deal with each type.

Number	Type	+5V	GND
IC1	7474	14	7
IC2	7416	14	7
IC3	7474	14	7
IC4	7474	14	7
IC5	7416	14	7

Table 1: Power wiring table for figures 2 and 3.

Instant Assembler

The operation sequence is as follows

1. Obtain op code mnemonic from terminal. The mnemonic must be terminated with a space and cannot be more than four characters long.
2. The mnemonic is searched for in the mnemonic table to see if it is valid.
3. The mnemonic type and basic op code are found. This causes a branch to an appropriate routine to obtain the correct parameters.
4. Having obtained the parameters, the complete op code plus additional words are calculated.
5. Finally, the completely assembled instruction is written into memory at the address given by last opened cell, and this variable is incremented by 2, 4 or 6, appropriately.

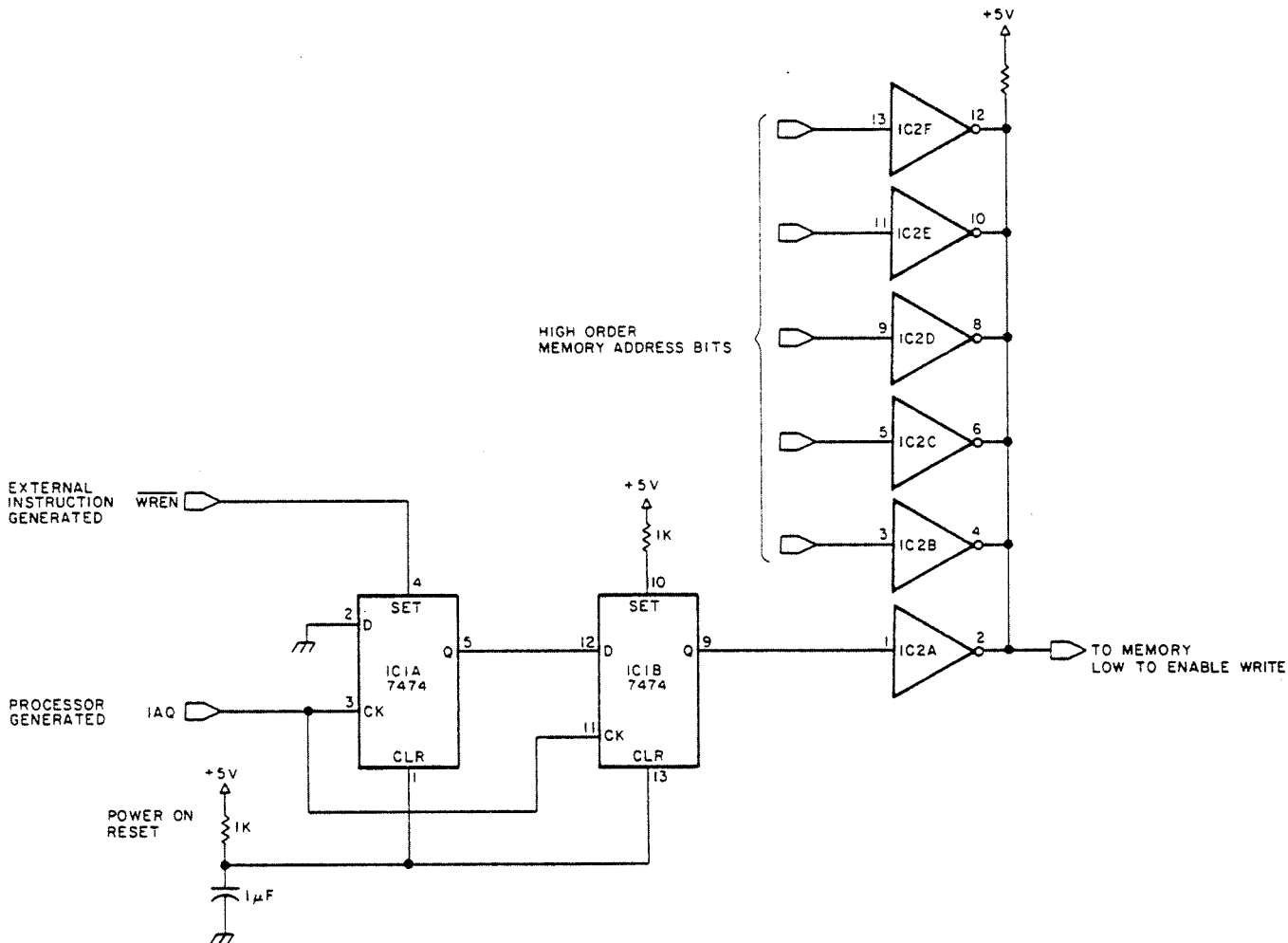


Figure 2: Hardware modification to write-protect the first 2 K bytes of memory in the TMS-9900 system. This prevents programs that are under development from overwriting themselves. After a write enable instruction, only the next instruction is able to write into the protected memory.

Disassembler

The disassembler is organized as a subroutine and is called by the instruction `XOP @ DISASM, 0`. This will disassemble the instruction pointed to by R12, after which R12 will have been incremented by 2, 4 or 6, appropriately. The sequence of operation is as follows

1. Determine the type of the instruction by systematically checking the op code.
2. Branch to the appropriate instruction type handler. Decode the parameters and output the disassembled instruction to the terminal.
3. Increment R12 appropriately and return.

The TRACE, EXECUTE AND STEP Instructions

These instructions require a small amount of additional hardware to cause an interrupt after the execution of an

instruction (See figure 3). The circuitry uses the processor-generated signal IAQ (instruction acquisition), which goes high every time the processor obtains an instruction from memory. Consider the return from interrupt mechanism used by the trace routines:

```

"
"
TRON (turns trace hardware on)
RTWP (returns to user program).

```

Note that TRON and TROFF are alternate names for the instructions LREX and CKOF, respectively, and they are used to turn the trace interrupt logic on and off. On returning from an interrupt, the circuitry must generate an interrupt request on the second IAQ pulse, the first pulse being generated by the RTWP instruction and the second by the actual user instruction. The advantage of this mechanism is that the user program does not have to be altered in any way whatsoever for it to be traced.

Op Code	Op Code	Op Code	Instruction Type	Op Code	Op Code	Instruction Type
OXXX -	00XX : ILL			1XXX -	10XX : JMP	
	01XX : ILL		-----		11XX : JLT	
	02XX -	020X : LI			12XX : JLE	
		022X : AI			13XX : JEQ	
		024X : ANDI	TYPE 6		14XX : JHE	
		026X : ORI			15XX : JGT	
		028X : CI	-----		16XX : JNE	
		02AX : STWP	TYPE 7		17XX : JNC	
		02CX : STST	-----		18XX : JOC	TYPE 4
		02EX : LWPI	-----		19XX : JNP	
	03XX -	030X : LIM1	TYPE 8		1AXX : JL	
		032X : ILL	-----		1BXX : JH	
		034X : IDLE			1CXX : JOP	
		036X : RSET			1DXX : SBO	
		038X : RTWP	TYPE 9		1EXX : SBZ	
		03AX : CKON	-----	2XXX -	20XX : COC	-----
		03CX : CKOF			24XX : CZC	
	04XX -	03EX : LREX	-----		28XX : XOR	
		040X : BLWP	-----		2CXX : XOP	TYPE 2
		044X : B			30XX : LDCR	
		048X : X		3XXX -	34XX : STCR	
		04CX : CLR			38CC : MPY	
	05XX -	050X : NEG			3CXX : DIV	-----
		054X : INV		4XXX : SZC		
		058X : INC		5XXX : SZCB		
		05CX : INCT	TYPE 3	6XXX : S		
	06XX -	060X : DEC	-----	7XXX : SB		
		064X : DECT		8XXX : C		
		068X : BL		9XXX : CB		
		06CX : SWPB		AXXX : A		TYPE 1
	07XX -	070X : SETO		BXXX : AB		
		074X : ABS		CXXX : MOV		
		078X : ILL		DXXX : MOVB		
		07CX : ILL	-----	EXXX : SOC		
	08XX : SRA			FXXX : SOCB		-----
	09XX : SRL		TYPE 5			
	OAXX : SLA		-----			
	OBXX : SRC					
	OCXX : ILL					
	ODXX : ILL					
	OEXX : ILL					
	OFXX : ILL					

Note: ILL = ILLEGAL OP CODE

Table 2: TMS-9900 op code structure. For the purposes of the authors' instant assembler and disassembler, the instruction set can be divided into nine basic types, and a subroutine can be written to deal with each type.

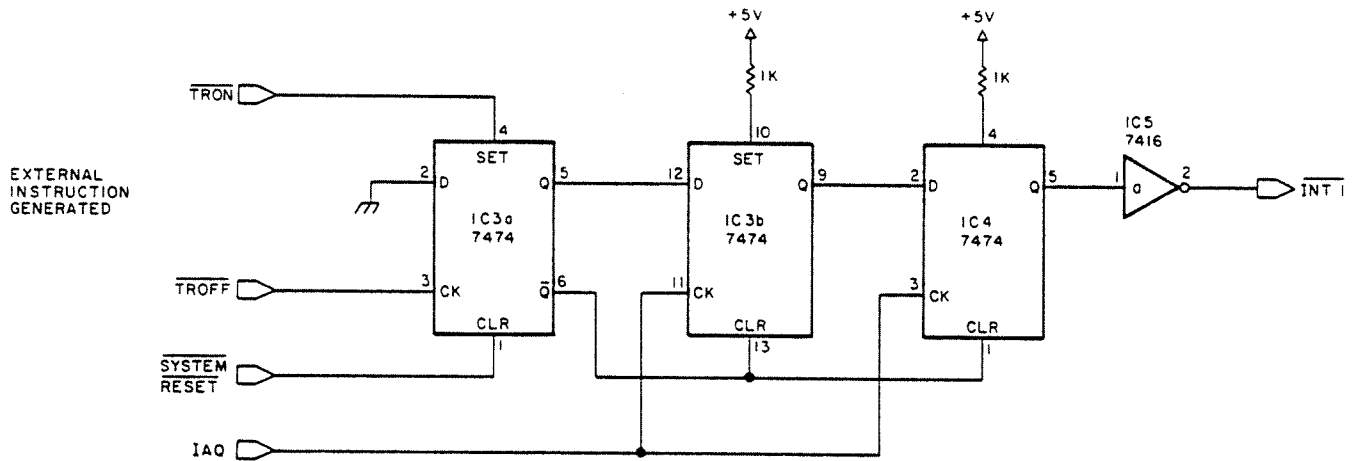


Figure 3: Hardware modification to the TMS-9900 system to enable the use of the authors' Trace, Execute, and Step instructions. The circuitry uses the processor-generated IAQ signal (instruction acquisition) which goes high every time the processor obtains an instruction from memory. TRON and TROFF are alternate names for the LREX and CKOF instructions, respectively. On returning from an interrupt, the circuitry must generate an interrupt request on the second IAQ pulse. With this modification, user programs can be traced without having to be altered.

The interrupt handler uses a separate workspace (INTIWP). Registers 2 thru 5 of this workspace contain the addresses of the breakpointed instructions, and it is the contents of these four registers that are changed by the H instruction. On acknowledgement of a trace interrupt, the appropriate handler, as determined by a pointer contained in R10, is executed. This pointer has been previously set up by the T, X and S routines. If the mode is Trace or Execute, then the breakpoint table is searched, if a match is found, the breakpoint message is output and control is passed back to the monitor. It is important to note that R15, R14 and R13 contain the environment of the traced program. In the step mode (S), a call to the disassembler is made. This displays in mnemonic form the last instruction executed together with the status register, and passes control back to the monitor. The user program may be examined or even changed before resuming execution.

The Move Instruction

The routine for this instruction is not quite as simple as it first seems. Basically, there are two ways of moving data from one part of memory to another to ensure that the new contents do not overwrite the old contents before the old contents have been moved (See figure 4).

Conclusion

This monitor has been in constant use for a period of several months debugging on-going projects (BASIC and Pascal interpreters) and has proved to be an invaluable working tool. ■

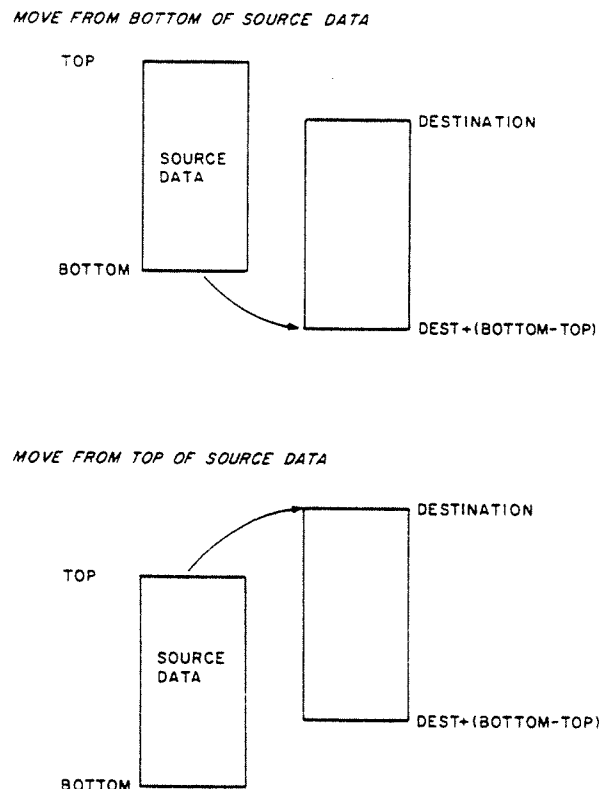


Figure 4: The two cases of the move instruction. Shown are the two ways to move data from one part of the memory to another to ensure that the new contents do not overwrite the old contents before the latter have been moved.

Listing 3: An example of the actual disassembler output.

```

0D40 02E0 LWPI #E020
0D44 020C LI R12, # 0060
0D48 1F01 TB #0001
0D4A 13FE JEQ #0D48
0D4C 1D08 SBO #0008
0D4E 04C4 CLR R4
0D50 D064 MOVB @ # C000 (R4), R1
0D54 9060 CB @ #0CEA, R1
0D58 141A JHE #0D8E
0D5A 06C1 SWPB R1
0D5C 0241 ANDI R1, #003F
0D60 C081 MOV R1, R2
0D62 0A22 SLA R2, 2
0D64 A042 A R2, R1
0D66 04C3 CLR R3
0D68 1F00 TB #0000
0D6A 16FE JNE #0D68
0D6C D161 MOVB @ # 0880 (R1), R5
0D70 0583 INC R3
0D72 0581 INC R1
0D74 1F04 TB #0004
0D76 16FE JNE # 0D74
0D78 3205 LDCR R5, 8
0D7A 1D0B SBO #000B
0D7C 1F04 TB #0004
0D7E 13FE JEQ #0D7C
0D80 0283 CI R3, #0005
0D84 1AF3 JL #0D6C
0D86 0584 INC R4
0D88 8804 C R4, @ #7FC8
0D8C 1AE1 JL #0D50
0D8E 1E08 SBZ #0008
0D90 1D0A SBO #000A
0D92 04C2 CLR R2
0D94 0403 CLR R3
0D96 1020 SBO #0009
0D98 0582 INC R2
0D9A 0583 INC R3
0D9C 0283 CI R3, #0200
0DA0 1AFC JL #0D9A
0DA2 1E09 SBZ #0009
0DA4 04C3 CLR R3
0DA6 0583 INC R3
0DA8 0283 CI R3, #0600
0DAC 1AFC JL #0DA6
0DAE 0282 CI R2, #0009
0DB2 1AF0 JL #0D94
0DB4 1F01 TB #0001
0DB6 13FE JEQ #0DB4
0DB8 1E0A SBZ #000A
0DBA 2C40 XOP R0, 1

```

Listing 4: The Texas Instruments' TMS-9908 monitor

*12/11 11:44
TMS-9908
18.1
180*

*** TMS9900 CROSS ASSEMBLER ***

>> TMS9900 MONITOR V2.0 <<

ADDR OP OP1 OP2 ST.NO.

10 PRINT
20 CREF
30 N00RJ
40 *****
50 *
60 * TMS9900 MONITOR, INSTANT ASSEMBLER AND DISASSEMBLER *
70 *
80 *****
90 *

TITLE=>> TMS9900 MONITOR V2.0 <<

0000	120 R0	E00	0		
0001	130 R1	F00	1		
0002	140 R2	F00	2		
0003	150 R3	E00	3		
0004	160 R4	E00	4		
0005	170 R5	F00	5		
0006	180 R6	F00	6		
0007	190 R7	F00	7		
0008	200 R8	F00	8		
0009	210 R9	F00	9		
000A	220 R10	F00	10		
000B	230 R11	F00	11		
000C	240 R12	F00	12		
000D	250 R13	F00	13		
000E	260 R14	F00	14		
000F	270 R15	F00	15		
	280 *				
000D	290 CR	E00	#0D		CARRIAGE RETURN
000A	300 LF	F00	#0A		LINE FEED
F3E0	310 *				
	320 INTIMP	F00	#F3E0		

F47C	F546	F568	F56E	850	XOPTAB	WORD	XOP0,XOP1,XOP2,XOP3,XOP4,XOP5
F482	F580	F590	F5A2	860	MESS00	TEXT	'> TMS9900 MONITOR <<'-
F48B	3E20	544D	5339	870	MESS01	RYTE	CR.LF,LF
F48E	3930	3020	4D4F	880	MESS02	TEXT	'>'-
F494	4E49	544F	5220	890	MESS03	RYTE	'??'-
F49A	3C3C	00		900	MESS04	TEXT	LF,CR,LF
F49D	0D	0A0A		910			'>'-
F4A0	2020	2020	3E00	920	MESS04	TEXT	' STACK OVERFLOW AT'-'
F4A6	203F	3F00		930	ATHASH	RYTE	'@'
F4AA	0A0D	0A		940	HASH	RYTE	'@',0
F4AD	20	2020	2020	950	COMMA	RYTE	'.',0
F4H2	00			960	R	TEXT	'.'
F4H3	20	5354	4143	970	RR	RYTE	'R',0
F4H8	4B20	4F56	4552	980	HEXTAB	EVEN	'0123456789ABCDEF'
F4HE	464C	4F57	2041	990	NUMTAB	TEXT	'0 1 2 3 4 5 6 7 8 9 101112131415'
F4C4	5400			1000			
F4C6	40			1010	INTAB	RYTE	#1A
F4C7	23	00		1020		TEXT	'////GMZQWRPXRHST/.'
F4C9	2C	00		1030	SUBTAB	WORD	MONITOR,0.0.0.0.GO.MOVE.FIND.OUTPUT.WP.REGIST.PRINT
F4CB	20	2020	2020	1040		WORD	XCUTE,LOW.SETAP,STEP,TRACE.CHAR,INSTANT.OPFN
F4D0	5200			1050	* MASK32	WORD	#001F
F4D2	3031	3233	3435	1060	MASK16	WORD	#000F
F4D8	3637	3839	4142	1070	MASKR	WORD	#0007
F4DF	4344	4546		1080	MASK3	WORD	#0003
F4F2	3020	3120	3220	1100	* XOP0		
F4F8	3320	3420	3520	1110	* XOP0		
F4FE	3620	3720	3820	1120	* SUAROUTINE	CALLING MECHANISM	
F4F4	3920	3130	3131	1130	* SUAROUTINE	CALLING MECHANISM	
F4FA	3132	3133	3134	1140	* SUAROUTINE	CALLING MECHANISM	
F500	3135						
F502	1A						
F503	2F	2F2F	2F47				
F508	4D5A	4F57	5250				
F50F	584C	4853	542F				
F514	2E20						
F516	F446	0000	0000				
F51C	0000	0000	FF76				
F522	FDRA	FE28	FE80				
F528	FE96	FB16	FB64				
F52E	FA4C	FE80	FA02				
F534	FAS2	FA40	F638				
F53A	F7DE	F5F4					
F53F	001F						
F540	000F						
F542	0007						
F544	0003						

F546	0360	XOP0	RSET	R10	DECREMENT STACK POINTER
F548	064A		DECT	R10,R9	CHECK FOR STACK OVERFLOW
F54A	824A		C	XOP0AA	
F54C	1B0A		JH	WORKSP	STACK OVERFLOW HANDLER
F54E	02E0	XOP0XX	LMPI	A2*14+XOPWP,R2	
F552	C0A0	MOV	MOV	@MESS04.3	
F556	2CE0	XOP	XOP	@HEXOUTX	
F55A	06A0	RL	R	@INIT2	
F55E	0460	XOP0AA	MOV	R14,*R10	PUSH RETURN ADDRESS ON STACK
F562	C68E	MOV	MOV	R11,R14	
F564	C38B		RTWP		
F566	0380	*			
		* XOP1			
		*			
		* SUBROUTINE RETURN MECHANISM			
		*			
		XOP1	RSET	*R10~,R14	
F568	0360	MOV	MOV		
F56A	C38A	RTWP	RTWP		
F56C	0380	*			
		* XOP2			
		*			
		* TERMINAL INPUT EXTENDED OPERATION			
		*			
F56E	0360	XOP2	RSET	R12,#40	SET CRU BASE ADDRESS
F570	020C	LI	LI	#08	SPIN ON TERMINAL STATUS
F574	1F08	XOP2AA	TR	XOP2AA	
F576	16FF	JNE	JNE	R0.7	PARITY BIT=0 ALWAYS
F578	35C0	STCR	STCR	#08	
F57A	1F08	SBZ	SBZ	R0,*R11	
F57C	D6C0	MOVH	MOVH		
F57E	0380	RTWP	RTWP		
		*			
		* XOP3			
		*			
		* TERMINAL OUTPUT EXTENDED OPERATION			
		*			
F580	0360	XOP3	RSET	R12	SET CRU BASE ADDRESS
F582	04CC	CLR	CLR	#0H	SPIN ON TERMINAL STATUS
F584	1F08	XOP3AA	TR	XOP3AA	
F586	16FE	JNE	JNE	*R11~,H	
F588	323B	LDCR	LDCR	#08	
F58A	1E08	SHZ	SHZ	XOP3AA	
F58C	16FB	JNE	JNE		
F58E	0380	RTWP	RTWP		
		*			
		* XOP4			
		*			
		* PUSHES R0-RX ONTO STACK			
		*			
F590	0360	XOP4	RSFT		CONTINUE UNTIL #00 HAS BEEN OUTPUTED
F592	C040	MOV	MOV	R13,R1	

F594	064A		XOP4AA	DECT	R10		
F596	824A			C	R10,R9		
F598	12DA			JLE	XUPOXXX		
F59A	C6R1			MOV	*R1*,*R10		
F59C	82C1			C	R1,R11		
F59E	12FA			JLE	XOP4AA		
F5A0	0380			RTWP			
			*	XOP5			
			*	XOP5			
			*	POPS	RX-R0	OFF	STACK
			*				
F5A2	0360		XOP5	RSET			
F5A4	C6FA		XOP5AA	MOV	*R10*,*R11		
F5A6	064B			DECT	R11		
F5A8	834B			C	R11,R13		
F5AA	14FC			JHE	XOP5AA		
F5AC	0380			RTWP			
			*				
			*	SUBROUTINE	HEXIN		
			*	USES	R1,R2,R3,R4		
			*				
F5AE	04C1		HEXIN	CLR	R1		
F5B0	04C2			CLR	R2		
F5B2	0703			SETO	R3		
F5B4	2C81		HEXIN0	XOP	R1,2		
F5B6	2CC1			XOP	R1,3		
F5B8	0204 000F			LI	R4,15		
F5BC	9064 F4D2		HEXIN1	CB	@HEXTAB(R4),R1		
F5C0	1604			JNE	HEXIN2		
F5C2	04C3			CLR	R3		
F5C4	0A42			SLA	R2,4		
F5C6	F084			SOC	R4,R2		
F5C8	10F5			JMP	HEXIN0		
F5CA	0604		HEXIN2	DEC	R4		
F5CC	18F7			JOC	HEXIN1		
F5CE	C0C3			MOV	R3,R3		
F5D0	045B			RT			
			*				
			*	SUBROUTINE	HEXOUT		
			*	USES	R0,R1,R2,R3		
			*				
F5D2	2CE0 F4H1		HEXOUT	XOP	@MESS03+7,3		
F5D6	2D03		HEXOUT0	XOP	R3,4		
F5D8	04C0		HEXOUTX	CLR	R0		
F5DA	0203 0004			LI	R3,*R04		
F5DF	08C2		HEXOUT1	SRC	R2,12		
F5E0	C042			MOV	R2,R1		
F5F2	0241 000F			ANDI	R1,#000F		
F5F6	D021 F4D2		MOVH	@HEXTAB(R1),R0			
F5FA	2CC0		XOP	R0,3			
F5FC	0603		DEC	R3			
F5FF	16F7		JNE	HEXOUT1			

CHECK FOR STACK OVERFLOW

.INPUTS A HEX NO. INTO R2

SET FLAG
GET CHARACTER
PRINT CHARACTER

CLEAR FLAG
SHIFTS HEX DIGIT INTO R2

TEST HEXIN FLAG

OUTPUTS CONTENTS OF R2

PRINTS " "
SAVE R0-R3

Creates in 8080 assembly code. PAI
and type 8080. The 2080 R3, R0
See page

Address	Hex	Instruction	Comments
F5F0	2D43	XOP	R3.5
F5F2	045B	RT	RESTORE R3-R0
F5F4	1601	JNE	JNE ON HEXIN FLAG
F5F6	C382	MOV	R2,R14
F5F8	024E FFFF	ANDI	R14,FFFF
F5FC	2C20 F622	XOP	@PADDR,C,0
F600	06A0 F5AE	HL	@HEXIN
F604	1602	JNE	JNE ON HEXIN FLAG
F606	03A0	WREN	
F608	C782	MOV	R2,*R14
F60A	0281 2000	CI	R1,1
F60F	1602	OPEN03	
F610	05CE	INCT	R14
F612	10F4	JMP	OPEN01
F614	0281 2D00	CI	R1,1
F618	1602	JNE	MON00A
F61A	064E	DECT	R14
F61C	10EF	JMP	OPEN01
F61E	0460 F44E	B	@MON00
F622	2CE0 F4AB	XOP	@MESS03+1,3
F626	C08E	MOV	R14,R2
F628	06A0 F5D6	HL	@HEXOUT0
F62C	C09E	MOV	*R14,R2
F62E	06A0 F5D2	BL	@HEXOUT
F632	2CE0 F480	XOP	@MESS03+6,3
F636	2C40	XOP	R0,1
F638	1601	JNE	JNE ON HEXIN FLAG
F63A	C382	MOV	R2,R14
F63C	0203 2F00	LI	R3,1
F640	C10E	MOV	R14,R4
F642	2C20 F622	XOP	@PADDR,C,0
F646	2CC3	XOP	R3,3
F648	2C81	XOP	R1,2
F64A	0281 0800	CI	R1,#0800
F64E	1605	JNE	CHAR03
F650	H384	C	R4,R14
F652	13FA	JEQ	CHAR02
F654	2CC1	XOP	R1,3
F656	060E	DEC	R14
F658	10F7	JMP	CHAR02
F65A	C041	MOV	R1,R1
F65C	1303	JEQ	CHAR04
F65E	0281 2000	CI	R1,1
F662	1ADD	JL	MON00A
F664	03A0	WREN	
2210		* INSTRUCTION	
2220		OPEN	
2230		OPEN00	
2240		OPEN01	
2250		HL	
2260		JNE	
2270		WREN	
2280		MOV	
2290		CI	
2300		OPEN02	
2310		JNE	
2320		INCT	
2330		JMP	
2340		CI	
2350		JNE	
2360		DECT	
2370		JMP	
2380		CI	
2390		JNE	
2400		DECT	
2410		JMP	
2420		MON00A	
2430		B	
2440		* PADDR	
2450		XOP	
2460		MOV	
2470		HL	
2480		MOV	
2490		BL	
2500		XOP	
2510		XOP	
2520		* INSTRUCTION	
2530		* INSTRUCTION	
2540		CHAR	
2550		JNE	
2560		MOV	
2570		LI	
2580		MOV	
2590		XOP	
2600		XOP	
2610		XOP	
2620		CI	
2630		JNE	
2640		C	
2650		JEQ	
2660		XOP	
2670		DEC	
2680		JMP	
2690		MOV	
2700		JEQ	
2710		CI	
2720		JL	
2730		CHAR04	
2740		WREN	

F74A	5352	4120	5352				'SRA SRL SIA SRC '
F750	4C20	534C	4120				
F756	5352	4320					
F75A	4C49	2020	4149				'LI AI ANDIORI CI '
F760	2020	414F	4449				
F766	4F52	4920	4349				
F76C	2020						
F76E	5354	5750	5354				'STWPSTST'
F774	5354						'LWPILIMI'
F776	4C57	5049	4C49				'ILL?IDLERSETRTWPWRECKOFLREX'
F77C	4D49						
F77E	494C	4C3F	4944				TYPE1,TYPE2,TYPE3,TYPE4,TYPE5
F784	4C45	5253	4554				TYPE6,TYPE7,TYPE8,TYPE9,TYTAB
F78A	5254	5750	5752				10.8.4.6.6.3.3.3.3.0
F790	454E	434B	4F46				#4000,#2000,#0400,#1000,#0800,#0200,#02A0,#0320
F796	4C52	455B					T1,T2,T3,T4,T5,T6,T7,T8,T9
F79A	F67A	F6AA	F6CA				
F7A0	F70A	F74A					
F7A4	F75A	F76E	F776				
F7AA	F77E	F79A					
F7AE	0A08	0406	0603				
F7B4	0303	0300					
F7H8	4000	2000	0400				
F7HE	1000	0800	0200				
F7C4	02A0	02E0	0320				
F7CA	F89B	F8AC	F8CC				
F7D0	F8D4	F904	F91A				
F7D6	F92A	F934	F860				
F7DC	2E00						
F7DE	1601						
F7E0	C3B2						
F7E2	02A0						
F7E4	0220	0010					
F7E8	0208	2020					
F7EC	C248						
F7FE	2C20	F622					
F7F2	2CE0	F7DC					
F7F6	04C2						
F7F8	2C81						
F7FA	2CC1						
F7FC	0281	4100					
F800	1A08						
F802	0281	5A00					
F806	1B05						
F808	0C01						
F80A	0582						
F80C	0282	0005					
F810	16F3						
F812	2CE2	F4AD					
F816	0281	2000					
2950	TYPE5	TEXT					'SRA SRL SIA SRC '
2960	TYPE6	TEXT					'LI AI ANDIORI CI '
2970	TYPE7	TEXT					'STWPSTST'
2980	TYPE8	TEXT					'LWPILIMI'
2990	TYPE9	TEXT					'ILL?IDLERSETRTWPWRECKOFLREX'
3000	TYTAB	WORD					TYPE1,TYPE2,TYPE3,TYPE4,TYPE5
3010		WORD					TYPE6,TYPE7,TYPE8,TYPE9,TYTAB
3020	TYSHIFT	BYTE					10.8.4.6.6.3.3.3.3.0
3030	TYBASE	WORD					#4000,#2000,#0400,#1000,#0800,#0200,#02A0,#0320
3040	TYSUB	WORD					T1,T2,T3,T4,T5,T6,T7,T8,T9
3050	DOT	WORD					
3060	*						
3070	*	GET MNEMONIC AND PLACE IN R8,R9					
3080	*						
3090	INSTANT	JNE					JNE ON HEXIN FLAG
3100		MOV					
3110	INST00	STWP					
3120		AI					R0->R8
3130		LI					INITIALISE R8,R9
3140		MOV					
3150		XOP					PRINT ADDRESS AND CONTENTS
3160		XOP					PRINT '*'
3170		CLR					CHARACTER COUNT=0
3180	INST01	XOP					GET CHARACTER
3190		XOP					PRINT CHARACTER
3200		CI					IN ['A'..'Z'] ?
3210	INST02	JL					
3220		CI					
3230		JH					
3240		MOVB					
3250		INC					
3260		CI					
3270		JNE					
3280	INST02	XOP					GET AT MOST 5 CHARACTERS
3290		CI					PRINT REMAINING SPACES
							CHECK LAST CHARACTER

Label	Hex	Op Code	Mnemonic	Type	Calculation	BASIC Op Code
F81A	1H3B		JH			ERROR
F81C	1A2E		JL			INST10
F81E	0207	F67A	LI			R7,TYPE1
F822	8217		C			*R7,R8
F824	1603		JNE			INST04
F826	8267	0002	C			#0002(R7),R9
F82A	1306		JEQ			INST05
F82C	0227	0004	AI			R7,4
F830	0287	F79A	CI			R7,TYTAB
F834	16F6		JNE			INST03
F836	102D		JMP			ERROR
F838	04C4		CLR			R4
F83A	0203	F79C	LI			R3,TYTAB+2
F83E	8CC7		C			R7,*R3
F840	1A02		JL			INST07
F842	0584		INC			R4
F844	10FC		JMP			INST06
F846	61E3	FFFC	S			#-4(R3),R7
F84A	0024	F7AF	MOV			RTYSHIFT(R4),R0
F84E	0980		SRL			R0,8
F850	0A07		SLA			R7,0
F852	0A14		SLA			R4,1
F854	A1E4	F788	A			RTYBASE(R4),R7
F858	04CA		CLR			R10
F85A	C024	F7CA	MOV			RTYSUB(R4),R0
F85E	0450		B			*R0
F860	2CB1		XOP			R1,2
F862	2CC1		XOP			R1,3
F864	02A0		STWP			R0
F866	0220	000F	AI			R0,2*7
F86A	03A0		WREN			*R0,*R14
F86C	CFR0		MOV			R10
F86E	060A		DEC			INST09
F870	18FC		JDC			R1,,
F872	0281	2000	CJ			ERROR
F876	180D		JH			INST00
F878	13H4		JEQ			#MON00
F87A	0460	F44E	B			
F87E	0281	2C00	CI			R1,,
F882	1304		JEQ			DELIM00
F884	0281	2000	CI			R1,,
F888	1804		JH			ERROR
F88A	1AF7		JL			INST10
F88C	2CF0	F4C9	XOP			@COMMA,3
F890	0458		RT			

RETURN TO MONITOR

* SEARCH FOR MNEMONIC IN TABLE

* FIND MNEMONIC TYPE AND CALCULATE BASIC OP CODE

R4:=OPTYPE

SHIFTS BY VALUE IN R0

R7:=BASIC OP CODE

NO OF WORDS IN INSTRUCTION

GET CHARACTER
PRINT LAST CHARACTER

R0->R7

RETURN TO MONITOR

CHECK CH IN ('','.',',')

PRINT ",."

Address	Instruction	OpCode	Register	Comment	Instruction Type
F892	2CE0 F4A6				INSTRUCTION OF TYPE1
F896	10A5				INSTRUCTION OF TYPE1
F898	06A0 F97C				INSTRUCTION OF TYPE2
F89C	06A0 F87E				INSTRUCTION OF TYPE2
F8A0	A1C0				INSTRUCTION OF TYPE2
F8A2	06A0 F97C				INSTRUCTION OF TYPE2
F8A6	0A60				INSTRUCTION OF TYPE2
F8AB	A1C0				INSTRUCTION OF TYPE2
F8AA	10DH				INSTRUCTION OF TYPE2
F8AC	06A0 F97C				INSTRUCTION OF TYPE2
F8B0	06A0 F87E				INSTRUCTION OF TYPE2
F8B4	A1C0				INSTRUCTION OF TYPE2
F8B6	0287 2C00				INSTRUCTION OF TYPE2
F8BA	1A03				INSTRUCTION OF TYPE2
F8BC	0287 3400				INSTRUCTION OF TYPE2
F8C0	1A02				INSTRUCTION OF TYPE2
F8C2	2CE0 F4D0				INSTRUCTION OF TYPE2
F8C6	06A0 F942				INSTRUCTION OF TYPE2
F8CA	10ED				INSTRUCTION OF TYPE2
F8CC	06A0 F97C				INSTRUCTION OF TYPE2
F8D0	A1C0				INSTRUCTION OF TYPE2
F8D2	10C7				INSTRUCTION OF TYPE2
F8D4	2CE0 F4C7				INSTRUCTION OF TYPE2
F8D8	06A0 F5AE				INSTRUCTION OF TYPE2
F8DC	16C2				INSTRUCTION OF TYPE2
F8DE	0287 1D00				INSTRUCTION OF TYPE2
F8E2	140B				INSTRUCTION OF TYPE2
F8E4	60BE				INSTRUCTION OF TYPE2
F8E6	0642				INSTRUCTION OF TYPE2
F8E8	0B12				INSTRUCTION OF TYPE2
F8EA	0282 FF80				INSTRUCTION OF TYPE2
F8EE	1178				INSTRUCTION OF TYPE2
F8F0	0282 007F				INSTRUCTION OF TYPE2
F8F4	1575				INSTRUCTION OF TYPE2
F8F6	0242 00FF				INSTRUCTION OF TYPE2
F8FA	0282 00FF				INSTRUCTION OF TYPE2
F8FE	1B70				INSTRUCTION OF TYPE2
F900	A1C2				INSTRUCTION OF TYPE2
F902	10AF				INSTRUCTION OF TYPE2
F904	2CE0 F4D0				INSTRUCTION OF TYPE2
F908	06A0 F942				INSTRUCTION OF TYPE2
F90C	06A0 F87E				INSTRUCTION OF TYPE2
F910	A1C0				INSTRUCTION OF TYPE2
F912	06A0 F942				INSTRUCTION OF TYPE2
F916	0A40				INSTRUCTION OF TYPE2
F918	10C7				INSTRUCTION OF TYPE2

PRINT " ??"

INSTRUCTION OF TYPE1

INSTRUCTION OF TYPE2

CHECK IF XOP,LDCR OR STCR

PRINT "R"

INSTRUCTION OF TYPE3

INSTRUCTION OF TYPE4

DEFAULT FOR JUMP INSTRUCTIONS IS INSTRUCTION SBO,SBZ,TB

CALCULATE JUMP OFFSET

INSTRUCTION OF TYPE5

INSTRUCTION OF TYPE 6	INSTRUCTION OF TYPE 7	INSTRUCTION OF TYPE 8	OBTAINS REGISTER NO OR CONSTANT NUMBER	R6->R5	AT MOST 3 CHARACTERS GET CHARACTER IN ('0'..'9') ?	PRINT CHARACTER	TEST IF VALID NUMBER	R0:=HEX EQUIVALENT OF DECIMAL NUMBER	OBTAINS SIXBIT SOURCE/DESTINATION FIELD
F91A 2CE0 F4D0	4360 T6	XOP	ARR,3						
F91E 06A0 F942	4370 RL	RL	@GETRC						
F922 A1C0	4380 A	A	R0,R7						
F924 06A0 F87F	4390 RL	RL	@DELIM						
F928 1005	4400 JMP	JMP	T8						
F92A 2CE0 F4D0	4410 *								
F92E 06A0 F942	4420 T7	XOP	ARR,3						
F932 10RA	4430 RL	RL	@GETRC						
F934 2CE0 F4C7	4440 JMP	JMP	T101						
F938 06A0 F5AF	4450 *								
F93C C202	4460 T8	XOP	@HASH,3						
F93F 058A	4470 RI,	RI,	@HEXIN						
F940 1090	4480 MOV	MOV	R2,R8						
F942 02A6	4490 INC	INC	R10						
F944 0226 000A	4500 JMP	JMP	INST08						
F948 0205 2020	4510 *								
F94C 0202 0003	4520 * GETRC								
F950 2C81	4530 *								
F952 0281 3000	4540 GETRC	STWP	R6						
F956 1A07	4550 AI	AI	R6,2*5						
F958 0281 3900	4560 LI	LI	R5,1						
F95C 1A04	4570 LI	LI	R2,3						
F95E 2CC1	4580 GETRC00	XOP	R1,2						
F960 D081	4590 CI	CI	R1,'0'						
F962 0602	4600 JL	JL	GETRC01						
F964 16F5	4610 CI	CI	R1,'9'						
F966 0202 001E	4620 JH	JH	GETRC01						
F96A 8162 F4E2	4630 XOP	XOP	R1,3						
F96E 1303	4640 MOV	MOV	R1,*R6~						
F970 0642	4650 DEC	DEC	R2						
F972 18FH	4660 JNE	JNE	GETRC00						
F974 1035	4670 GETRC01	LI	R2,2*15						
F976 0912	4680 GETRC02	C	@NUMTAB(R2),R5						
F978 C002	4690 JEQ	JEQ	GETRC03						
F97A 045B	4700 DECT	DECT	R2						
F97C C30H	4710 JNC	JNC	GETRC02						
F97E 2C81	4720 JMP	JMP	FERR						
F980 2CC1	4730 GETRC03	SRL	R2,1						
F982 0281 5200	4740 MOV	MOV	R2,R0						
F986 1603	4750 RT	RT							
F988 06A0 F942	4760 *								
F98C 045C	4770 * SIX								
F98E 0281 2A00	4780 *								
F992 160E	4790 SIX	MOV	R11,R12						
F994 2CF0 F4D0	4800 XOP	XOP	R1,2						
	4810 XOP	XOP	R1,3						
	4820 CI	CI	R1,'R'						
	4830 JNE	JNE	SIX00						
	4840 RL	RL	@GETRC						
	4850 R	R	*R12						
	4860 SIX00	SIX00	R1,'*'						
	4870 CI	CI	SIX03						
	4880 JNE	JNE	ARR,3						
	4890 XOP	XOP							

Address	Op Code	Instruction	Comments
F99B	06A0	F942	@GETRC
F99C	0220	0010	R0.#10
F9A0	0281	2000	R1.#1
F9A4	1604		SIX02
F9A6	2CC1		R1.3
F9AB	0220	0020	R0.#20
F9AC	2C81		R1.2
F9AE	045C		#R12
F9B0	0281	4000	R1.#0
F9B4	1615		ERR1
F9B6	2CE0	F4C7	@HASH,3
F9BA	06A0	F5AE	@HEXIN
F9BE	C28A		R10.R10
F9C0	C202		SIX04
F9C2	C202		R2.R8
F9C4	1001		SIX05
F9C6	C242		R2.R9
F9C8	058A		R10
F9CA	04C0		R0
F9CC	0281	2800	R1.#1
F9D0	1608		SIX07
F9D2	2CE0	F4D0	@RR,3
F9D6	06A0	F942	@GETRC
F9DA	0281	2900	R1.#1
F9DE	1302		SIX06
F9E0	0460	F892	@ERROR
F9E4	2CC1		R1.3
F9E6	2C81		R1.2
F9E8	0220	0020	R0.#20
F9FC	045C		#R12
F9FE	000A	0A	
F9F1	20	2020 2020	CR,LF,LF
F9F6	4250	2000	BP
F9FA	2053	4554 2041	SET AT
FA00	5400		
FA02			
FA06	0205	F3F0	R5.INT1MP
FA06	0225	0008	R5.2#4
FA0A	0208	3100	R8.#1
FA0E	2CE0	F9EF	@SMESS0,3
FA12	2CC8		R8.3
FA14	2CE0	F9FA	@SMESS1,3
FA18	C0B5		#R5.R2
FA1A	06A0	F5D2	@HEXOUT
FA1E	2CE0	F480	@MESS03+6,3
FA22	06A0	F5AE	@HEXIN
FA26	1602		SETBP01
FA28	C942	FFFF	R2.#-2(R5)
FA2C	0281	2000	R1.#1
5190	*		ERR1
5200	*		SIX06
5210	*	INSTRUCTION H	
5220	*		SIX07
5230	SMESS0	BYTE	
5240	TEXT		
5250	SMESS1	TEXT	
5260	EVEN		
5270	SETBP	LI	
5280	AI		
5290	LI		
5300	SETBP00	XOP	
5310	XOP		
5320	XOP		
5330	MOV		
5340	HL		
5350	XOP		
5360	HL		
5370	JNE		
5380	MOV		
5390	SETBP01	CI	

AMODE:=1
 AUTO INCREMENT REGISTER MODE
 PRINT "#"
 AMODE:=3
 GET CHARACTER
 RETURN
 SYMBOLIC ADDRESSING MODE
 PRINT "#"
 TEST R10

INDEXED ADDRESSING MODE
 PRINT "R"

PRINT ")*"
 GET CHARACTER
 AMODE:=2

R5->BP 1 (R4,R5,R6,R7)

PRINT " BP "
 PRINT BP NO.
 PRINT "SET AT"
 PRINT CONTENTS

PRINT " "
 GET NEW CONTENTS(IF REQUIRED)
 JNE ON HEXIN FLAG

FA30	1605		JNE	SETBP02	
FA32	0228	0100	AI	R8.#100	
FA36	0288	3500	CI	R8.#5	
FA3A	16E9		JNE	SETBP00	
FA3C	0460	F44E	R	@MON00	
			*		
			*	INSTRUCTION T	
FA40	0204	0020	LI	R4.#20	
FA44	C804	F3F2	MOV	R4.#2*9+INT1WP	
FA48	04C4		CLR	R4	
FA4A	1005		JMP	SETUP	
			*		
			*	INSTRUCTION X	
FA4C	0204	0002	LI	R4.2	
FA50	1002		JMP	SETUP	
			*		
			*	INSTRUCTION S	
FA52	0204	0004	LI	R4.4	
			*		
FA56	0360		RSET		DISABLE INTERRUPTS
FA58	C804	F3F4	MOV	R4.#2*10+INT1WP	
FA5C	C0C3		MOV	R3,R3	TEST HEXIN FLAG
FA5E	1602		JNE	SETUP00	
FA60	C802	F3FC	MOV	R2.#2*14+INT1WP	SET UP NEW START ADDRESS
FA64	02E0	F3F0	MOV	INT1WP	CHANGE WORKSPACE.
FA68	C30E		MOV	R14,R12	ADDRESS OF NEXT INSTRUCTION
FA6A	27E0	F540	CZC	@MASK16,R15	
FA6E	1601		JNE	SETUP01	
FA70	058F		INC	R15	
FA72	03C0		TRON		TURN TRACE LOGIC ON
FA74	0380		RTWP		RETURN TO PROGRAM BEING TRACED
			*		
			*	INTERRUPT 1 HANDLER	
FA76	FA96	FAC0	WORD	INT1AA,INT1CC,INT1GG	TRACE,XCUTE,STEP
FA7C	3D3E	00	TEXT	'=>'	
FA7F	20	2057	TEXT	' WP='	
FA84	00		TEXT		
FA85	18	2C	BYTE	#18.#2C	CONTROL CHARACTERS TO POSITION CURSER
FA87	20	2053	TEXT	' ST='	
FA8C	00		TEXT		
FA8D	00		EVEN		
FA8F	03E0		TROFF		RESET TRACE LOGIC
FA90	C02A	FA76	MOV	@I1TAB(R10),R0	JUMP TO APPROPRIATE HANDLER
FA94	0450		B	*R0	
			*		
FA96	2660	F542	CZC	@MASKR,R9	
FA9A	1602		JNE	INT1BB	
FA9C	2CE0	F48F	XOP	@MESS03+1.3	PRINT INDENTATION
FAA0	C08C		MOV	R12,R2	PRINT ADDRESS OF LAST INST.

F4A2	06A0	F5D6	BL	@HEXOUT0			PRINT ">"
F4A6	2CE0	F47C	XOP	@IMESS0,3			
F4AA	0609		DEC	R9			
F4AC	1609		JNE	INT1CC			
F4AE	04C1		CLR	R1			
F4B0	2CE0	F4A8	XOP	@MESS02+2,3			PRINT "?" GET CHARACTER
F4B4	2C81		XOP	R1,2			
F4B6	0281	2000	CI	R1,1			
F4BA	161F		JNE	INT1FF			
F4BC	0209	0020	LI	R9,#20			
F4C0	0202	0004	* INT1CC	R2,4			
F4C4	02A0		STWP	R0			
F4C6	0220	0008	AI	R0,2*4			R0->BP 1
F4CA	8330		C	*R0,R12			IS INSTRUCTION BREAKPOINTED?
F4CC	1305		JEQ	INT1EE			
F4CE	0602		DEC	R2			
F4D0	16FC		JNE	INT1DD			
F4D2	C30E		MOV	R14,R12			R12:=ADDRESS OF NEXT INSTRUCTION
F4D4	03C0		TRON				TURN TRACE LOGIC ON
F4D6	0380		RTWP				RETURN TO USER PROGRAM
F4E8	2CE0	F9EE	* INT1EE	XOP	@SMESS0,3		PRINT INDENTATION+ BP*
F4EC	2CE0	F9FF	XOP	XOP	@SMESS1+5,3		PRINT "AT"
F4F0	C08C		MOV	R12,R2			
F4F2	06A0	F5D2	BL	@HEXOUT			
F4F6	2CE0	F47F	XOP	@IMESS1,3			PRINT "WP="
F4FA	C08D		MOV	R13,R2			PRINT USER WORKSPACE
F4FC	06A0	F5D6	BL	@HEXOUT0			
F4F0	2CE0	F487	XOP	@IMESS3,3			PRINT "ST="
F4F4	C08F		MOV	R15,R2			PRINT USER STATUS
F4F6	06A0	F5D6	BL	@HEXOUT0			
F4FA	02E0	F3A0	INT1FF	LMPI	WORKSP		
F4FE	0460	F44E	R	@MON00			
F402	2D0B		* INT1GG	XOP	R11,4		SAVE R0-R11
F404	2C20	F8DA	XOP	@DISASM,0			
F408	2D4B		XOP	R11,5			RESTORE R0-R11
F40A	2CE0	F4A5	XOP	@IMESS2,3			PRINT "ST="
F40E	C08F		MOV	R15,R2			PRINT STATUS
F410	06A0	F5D6	BL	@HEXOUT0			
F414	10F2		JMP	INT1FF			RETURN TO MONITOR
F416	0204	F3FA	* INSTRUCTION R				
F41A	C114		* REGIST	LI	R4,2*13+INT1WP		
F41C	C0C3		MOV	*R4,R4			R4:=USER WORKSPACE BY DEFAULT
F41E	1601		MOV	R3,R3			TEST HEXIN FLAG
F420	C102		JNE	REGIST0			
F422	0244	FFFF	MOV	R2,R4			
F430	REGIST0	AND1		R4,FFFF			MAKE ADDRESS EVEN

FB26	2CE0	F4AA	XOP	MESS03,3	PRINT INDENTATION
FB2A	2CE0	FAB1	XOP	MESS1+2,3	PRINT "WP="
FB2E	C084		MOV	R4,R2	PRINT WORKSPACE ADDRESS
FB30	06A0	F5D6	BL	HEXOUT0	
FB34	04C5		CLR	R5	
FB36	04CA		CLR	R10	
FB38	0209	0D0A	REGIST1	R9,#0D0A	
FB3C	2CC9		XOP	R9,3	PRINT CR,LF
FB3E	2CF0	F4C8	XOP	R9,3	PRINT "R"
FB42	C265	F4F2	REGIST2	R9,3	PRINT REGISTER NO
FB46	2CC9		MOV	NUMTAB(R5),R9	
FB48	0209	203D	XOP	R9,3	
FB4C	2CC9		LI	R9,3	
FB4E	C084		XOP	R4,R2	PRINT REGISTER CONTENTS
FB50	06A0	F5D2	MOV	HEXOUT	
FB54	05C5		RI	R5	
FB56	0285	0020	INCT	R5	
FB5A	1315		CJ	R5,#20	
FB5C	2560	F544	JEQ	MON00B	
FB60	13EB		CZC	MASK3,R5	
FB62	10ED		JEQ	REGIST1	
			JMP	REGIST2	
			*	INSTRUCTION P	
			*		
FB64	C30F		PRINT	R15,R12	DEFAULT ACTION
FB66	C0C3		MOV	R3,R3	TEST HEXIN FLAG
FB68	1601		JNE	PRINT00	
FB6A	C302		MOV	R2,R12	
FB6C	024C	FFFF	PRINT00	R12,0FFFF	INSTRUCTION COUNT
FB70	020D	0010	LI	R13,16	CALL DISASSEMBLER
FB74	2C20	F8DA	PRINT01	DISASM,0	DECREMENT INSTRUCTION COUNT
FB78	060D		DEC	R13	
FB7A	16FC		JNE	PRINT01	
FB7C	058D		INC	R13	
FB7E	2C81		XOP	R1,2	GET CHARACTER
FB80	0281	2000	CI	R1,1	MORE INSTRUCTIONS TO BE DISASSEMBLED ?
FB84	13F7		JEQ	PRINT01	
FB86	0460	F44F	MON00B	MON00	
			*		
			*		
			*		
			*		
			*		
			*		
			*		
FB88	FC70	FC7A	COATAB	WORD	CODE00, CODE02, CODE04, CODE04, CODE04, CODE04
FB90	FC7A	FC94	WORD		CODE08, CODE08, CODE08, CODE00, CODE00, CODE00, CODE00
FB96	FC94	FC94	WORD		DSIX, DSIX, DSIX, KSIX, KSIX, DSIX, DSIX
FB9A	FC80	FC80	WORD		IMMR, IMMR, IMMR, IMMR, ONER, IMM
FBA0	FC80	FC70	WORD		
FBA6	FC70	FC70	WORD		
FBA8	FC88	FC88	WORD		
FBAE	FCEA	FCEA	WORD		
FBB0	FC88	FC88	WORD		
FBB6	FCF6	FCF6	WORD		
FBB8	FCF6	FCF6	WORD		

IMM.DISASM2.DISASM2.DISASM2.DISASM2.DISASM2.DISASM2.DISASM2

FCR6	FCF6	FD0C	WORD	PRINT INDENTATION
FRC6	FCF6	FD0C		
FBCA	FD00	FC3A		
FBD0	FC3A	FC3A		
FBD6	FC3A	FC3A		
FHDA	2CE0	F4A8		
FHDE	C08C			
FHF0	06A0	F5D6		
FHF4	C17C			
FHF6	C085			
FHFB	06A0	F5D2		
FHFC	C185			
FHFE	09C6			
FHF0	1605			
FHF2	C185			
FHF4	0976			
FHF6	C026	F88A		
FHFA	0450			
FHFC	0286	0003		
FC00	1B2A			
FC02	0286	0002		
FC06	141A			
FC08	C005			
FC0A	0240	0F00		
FC0E	0960			
FC10	0220	F70A		
FC14	06A0	FD8C		
FC18	C085			
FC1A	0242	00FF		
FC1E	0285	1D00		
FC22	1407			
FC24	0282	0080		
FC28	1A02			
FC2A	0262	FF00		
FC2E	0A12			
FC30	A08C			
FC32	2CE0	F4C7		
FC36	06A0	F5D6		
FC3A	2C40			
FC3C	C185			
FC3E	0246	1C00		
FC42	0986			
FC44	C006			
FC46	0220	F6AA		
FC4A	06A0	FD8C		
FC4E	0916			
FC50	C026	F8AA		
FC54	0450			
FC56	0A26			
6910				
6920	*	DISASM	XOP	MESS03+1.3
6930		MOV	BL	R12,R2
6940		MOV	BL	HEXOUT0
6950		MOV	MOV	R12,R5
6960		MOV	BL	R5,R2
6970		MOV	BL	HEXOUT
6980		MOV	SRL	R5,R6
6990		SRL	JNE	R6,12
7000		JNE	MOV	DISASM1
7010		MOV	SRL	R5,R6
7020		SRL	MOV	R6,7
7030		MOV	B	COATAB(R6),R0
7040		B	CI	R0
7050		CI	JH	R6,#0003
7060		JH	CI	CODE4
7070		CI	JHE	R6,#0002
7080		JHE	MOV	CODE2
7090	*	DISASM1	MOV	R5,R0
7100		MOV	ANDI	R0,#0F00
7110		ANDI	SRL	R0,6
7120		SRL	AI	R0,TYPE4
7130		AI	BL	PMNEM
7140		BL	MOV	R5,R2
7150		MOV	ANDI	R2,#00FF
7160		ANDI	CI	R5,#1D00
7170		CI	JHE	CODE100
7180		JHE	CI	R2,#0080
7190		CI	JL	CODE101
7200		JL	ORI	R2,#FF00
7210		ORI	SLA	R2,1
7220		SLA	A	R12,R2
7230		A	XOP	HASH,3
7240		XOP	BL	HEXOUT0
7250		BL	XOP	R0,1
7260		XOP	MOV	R5,R6
7270	*	DISASM2	ANDI	R6,#1C00
7280		DISASM2	SRL	R6,8
7290		MOV	MOV	R6,R0
7300		MOV	AI	R0,TYPE2
7310		AI	BL	PMNEM
7320		BL	SRL	R6,1
7330		SRL	MOV	COATAB(R6),R0
7340		MOV	B	R0
7350		B	SLA	R6,2
7360	*	CODEF4	SLA	
7370		CODEF4		
7390				

PRINT "#"
RETURN

FC58	C006	7400	MOV	R6,R0	
FC5A	0220 F66A	7410	AI	R0,TYPE1-16	
FC5E	06A0 FD8C	7420	BL	APMNE:M	
FC62	2C20 FD30	7430	XOP	@SIXBITS.0	PRINT ".,"
FC66	2CE0 F4C9	7440	XOP	@COMMA.3	
FC6A	2C20 FD34	7450	XOP	@SIXBITD.0	RETURN
FC6E	2C40	7460	XOP	R0.1	
FC70	0200 F77E	7470	* CODE00	R0,TYPE9	
FC74	06A0 FD8C	7480	BL	APMNE:M	RETURN
FC78	2C40	7500	XOP	R0.1	
FC7A	C185	7510	* CODE02	R5,R6	
FC7C	0246 01E0	7520	MOV	R6,#01E0	
FC80	0936	7530	ANDI	R6.3	
FC82	C006	7540	SRL	R6,R0	
FC84	0220 F75A	7550	MOV	R0,TYPE6	
FC88	06A0 FD8C	7560	AI	APMNE:M	
FC8C	0916	7570	BL	R6.1	
FC8E	C026 FRHA	7580	SRL	@C02ATAB(R6).R0	
FC92	0450	7590	MOV	*R0	
FC94	C005	7600	R		
FC96	0240 03C0	7610	* CODE04	R5,R0	
FC9A	0940	7620	MOV	R0,#03C0	
FC9C	0220 F6CA	7630	ANDI	R0.4	
FCA0	06A0 FD8C	7640	SRL	R0,TYPE3	
FCA4	0285 07C0	7650	AI	APMNE:M	
FCA8	1402	7660	BL	R5,#07C0	
FCAA	2C20 FD30	7670	CI	CODE040	
FCAE	2C40	7680	JHE	@SIXBITS.0	RETURN
FCB0	C005	7690	XOP	R0.1	
FCB2	0240 0300	7700	XOP		
FCB6	0960	7710	* CODE08	R5,R0	
FCB8	0220 F74A	7720	MOV	R0,#0300	
FCBC	06A0 FD8C	7730	ANDI	R0.6	
FCC0	C185	7740	SRL	R0,TYPE5	
FCC2	2C20 FD14	7750	AI	APMNE:M	
FCC6	2CE0 F4C9	7760	BL	R5,R6	
FCCA	0935	7770	MOV	@REG.0	
FCCC	0245 001F	7780	XOP	@COMMA.3	PRINT ".,"
FCD0	C265 F4E2	7790	XOP	R5.3	PRINTS CONSTANT FIELD'
FCD4	2CC9	7800	SRL	R5.3	
FCD6	2C40	7810	CFIELD	R5.#001F	RETURN
FCDB	2C20 FD30	7820	MOV	@NUMTAB(R5).R9	PRINTS SIXBIT FIELD + REGISTER
FCDC	2CE0 F4C9	7830	XOP	R9.3	PRINT ".,"
FCE0	C185	7840	XOP	R0.1	
FCE2	0966	7850	* DSIX	@SIXBITS.0	
FCE4	2C20 FD14	7860	XOP	@COMMA.3	
FCE8	2C40	7870	XOP	R5,R6	
		7880	MOV	R6.6	
		7890	SRL	@REG.0	
		7900	XOP	R0.1	RETURN
		7910	XOP		
		7920	* DSIX		

FCFA	2C20	FD30	KSIX	XOP	@SIXBITS.0	PRINTS SIXBIT FIELD + CONSTANT
FCFE	2CE0	F4C9	7930	XOP	@COMMA.3	PRINT ".,"
FCF2	0955		7940	XOP	R5.5	
FCF4	10EH		7950	SRL	CFIELD	
			7960	JMP		
			7970			
			7980			
			7990			
FCF6	C185		8000	MOV	R5,R6	PRINTS REGISTER NO. + IMMEDIATE OPERAND
FCF8	2C20	FD14	8010	XOP	@REG.0	
FCFC	2CE0	F4C9	8020	XOP	@COMMA.3	PRINT ".,"
FD00	2CE0	F4C7	8030	XOP	@HASH.3	PRINT "##"
FD04	C0BC		8040	MOV	*R12~,R2	
FD06	06A0	F5D6	8050	BL	@HEXOUT0	RETURN
FD0A	2C40		8060	XOP	R0.1	
			8070			
FD0C	C185		8080	MOV	R5,R6	PRINTS REGISTER
FD0E	2C20	FD14	8090	XOP	@REG.0	RETURN
FD12	2C40		8100	XOP	R0.1	
			8110			
FD14	0246	000F	8120	ANDI	R6.,#000F	PRINTS REGISTER NO. E.G. R1,R10
FD18	0A16		8130	SI.A	R6.1	
FD1A	2CE0	F4D0	8140	XOP	@RR.3	PRINT "R"
FD1E	C266	F4E2	8150	MOV	@NUMTAB(R6),R9	
FD22	0286	0014	8160	CI	R6.#14	
FD26	1402		8170	JHE	REG00	
FD28	0249	FF00	8180	ANDI	R9.,#FF00	
FD2C	2CC9		8190	XOP	R9.3	RETURN
FD2E	2C40		8200	XOP	R0.1	
			8210			
FD30	C185		8220	MOV	R5,R6	DECODES AND PRINTS SIXBIT SOURCE FIELD
FD32	1002		8230	JMP	SIXBIT	
FD34	C185		8240	MOV	R5,R6	DECODES AND PRINTS SIXBIT DEST. FIELD
FD36	0966		8250	SRL	R6.6	
FD38	0246	003F	8260	ANDI	R6.,#003F	R6:=SIXBIT FIELD
FD3C	C1C6		8270	MOV	R6.R7	
FD3F	0246	000F	8280	ANDI	R6.,#000F	R6:=REGISTER
FD42	0947		8290	SRL	R7.4	R7:=ADDRESSING MODE
FD44	1603		8300	JNE	SIXBIT0	REGISTER ADDRESSING MODE
FD46	2C20	FD14	8310	XOP	@REG.0	REGISTER ADDRESSING MODE
FD4A	101F		8320	JMP	SIXBIT3	SYMBOLIC ADDRESSING MODE
FD4C	0287	0002	8330	CI	R7.#2	
FD50	1611		8340	JNE	SIXBIT2	PRINT "##"
FD52	2CE0	F4C6	8350	XOP	@ATHASH.3	
FD56	C0BC		8360	MOV	*R12~,R2	INDEXED ADDRESSING MODE
FD58	06A0	F5D6	8370	BL	@HEXOUT0	
FD5C	0286	0000	8380	CI	R6.,#0000	
FD60	1314		8390	JEQ	SIXBIT3	PRINT "("
FD62	0201	2800	8400	LI	R1.(')	
FD66	2CC1		8410	XOP	R1.3	
FD68	2C20	FD14	8420	XOP	@REG.0	
FD6C	0201	2900	8430	LI	R1.(')	
FD70	2CC1		8440	XOP	R1.3	PRINT ")"
FD72	100B		8450	JMP	SIXBIT3	

INDIRECT REGISTER ADDRESSING MODE:

FD74	0201	2A00	SIXBIT2	LI	R1,''
FD78	2CC1		XOP	R1.3	
FD7A	2C20	FD14	XOP	@REG,0	
FD7E	0287	0003	CI	R7.#0003	
FD82	1603		JNE	SIXBIT3	
FD84	0201	2R00	LI	R1,''	
FD88	2CC1		XOP	R1.3	
FD8A	2C40		SIXBIT3	XOP	
FD8C	2CE0	F4H0	*	R0.1	
FD90	04CA		PMNEM	@MESS03+6.3	
FD92	C230		CLR	R10	
FD94	C250		MOV	*R0^,R8	
FD96	2CC8		MOV	*R0,R9	
FD98	2CE0	F4H1	XOP	R8.3	
FD9C	045R		XOP	@MESS03+7.3	
			RT		

AUTO INCREMENT ADDRESSING MODEBT

RETURN

SUBROUTINE TO PRINT OPCODE MNEMONIC

PRINT "

FD9E	4F56	4520	4259	'OVE BYTES '-
FDA4	5445	5320	00	
FDA9	54	4F20	00	'TO '-
FDAD	59	4553	204F	'YES OR NO? '-
FDR2	5220	4E4F	3F20	
FDR8	00			

* INSTRUCTION M

FDB9	00		EVEN		
FDHA	2CE0	FD9E	XOP	@MESS0,3	PRINT "OVE BYTES "
FDHE	06A0	F5AE	RL	@HEXIN	JNF ON HEXIN FLAG
FDC2	162R		JNE	MOVE04	R8:=LOW ADDRESS
FDC4	C202		MOV	R2,R8	
FDC6	0281	3A00	CI	R1,''	ERROR
FDCA	1627		JNE	MOVE04	JNE ON HEXIN FLAG
FDCC	06A0	F5AE	RL	@HEXIN	
FDD0	1624		JNE	MOVE04	
FDD2	808R		C	R8,R2	
FDD4	1B22		JH	MOVE04	
FDD6	C1C2		MOV	R2,R7	R7:=HIGH ADDRESS
FDD8	2CE0	FDA9	XOP	@MESS1,3	PRINT " TO"
FDDC	06A0	F5AE	RL	@HEXIN	JNE ON HEXIN FLAG
FDE0	161C		JNE	MOVE04	
FDE2	C182		MOV	R2,R6	
FDE4	2CE0	F4AA	XOP	@MESS03,3	PRINT INDENTATION
FDE8	2CE0	FDAD	XOP	@MESS2,3	PRINT "YES OR NO?"
FDEC	2C81		XOP	R1.2	GET CHARACTER
FDFE	2CC1		XOP	R1.3	PRINT CHARACTER
FDF0	0281	5900	CI	R1,'Y'	RETURN TO MONITOR
FDF4	1610		JNE	MON00C	
FDF6	8206		C	R6,R8	LOW=DESTINATION
FDF8	130E		JEQ	MON00C	
FDFA	1R04		JH	MOVE01	
FDFC	DDR8		MOV	*R8^,*R6^	
FDFE	81C8		C	R8,R7	RETURN TO MONITOR
FF00	1R0A		JH	MON00C	

FE02 10FC
FE04 81C6
FE06 1BFA
FE08 6188
FE0A A187
FE0C D597
FE0E 0607
FE10 0606
FE12 8207
FE14 14FB
FE16 0460 F44F
FE1A 0460 F464

FE1F 4E4F 5420 464F
FE24 554E 4400

FE28

FE2A

FE2C

FE2E

FE30

FE32

FE34

FE36

FE38

FE3A

FE3C

FE40

FE44

FE46

FE4A

FE4C

FE4E

FE52

FE54

FE58

FE5C

FE60

FE62

FE64

FE68

FE6A

FE6C

FE6E

FE72

FE74

1000

0281 2000

13E2

1000

DA42 FFFF

06C2

03A0

0642

02B1 2000

13E2

1000

16CF

0452

8960 MOVE00
8970 R6,R7
8980 MOVE00
8990 R8,R6
9000 R7,R6
9010 *R7,*R6
9020 R7
9030 R6
9040 R7,R8
9050 MOVE02
9060 @MON00
9070 @MON02
9080 *
9090 * INSTRUCTION Z
9100 *
9110 FMESS00 TEXT

'NOT FOUND'-

9120 EVEN
9130 MOV
9140 MOV
9150 CB
9160 JNE
9170 SWPB
9180 CB
9190 SWPB
9200 JEQ
9210 C
9220 JNE
9230 XOP
9240 XOP
9250 JMP
9260 XOP
9270 MOV
9280 DEC
9290 BL
9300 MOV
9310 BL
9320 XOP
9330 HL
9340 JNE
9350 WREN
9360 MOV
9370 SWPB
9380 WREN
9390 MOV
9400 F
9410 CI
9420 JEQ
9430 JMP
9440 * INSTRUCTION G
9450 *
9460 GO
9470 B

R15,R9
R2,R7
R9,R7
FIND01
R7
*R9,R7
R7
FIND02
R9,R15
FIND00
@MESS03,3
@FMESS00,3
MON00C
@MESS03,3
R9,R2
R2
@HEXOUT0
R7,R2
@HEXOUT
@MESS03+6,3
@HEXIN
FIND03
R2,@#FFFF(R9)
R2
R2,*R9
R1,*R9
FIND01
MON00C

FIND01
FIND02
FIND03
FIND04
FIND05
FIND06
FIND07
FIND08
FIND09
FIND10
FIND11
FIND12
FIND13
FIND14
FIND15
FIND16
FIND17
FIND18
FIND19
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FIND88
FIND89
FIND90
FIND91
FIND92
FIND93
FIND94
FIND95
FIND96
FIND97
FIND98
FIND99
FIND100

PRINTS " ??"

PRINT INDENTATION
PRINT "NOT FOUND"
PRINT INDENTATION

PRINT ADDRESS

PRINT CONTENTS
PRINT " " "

JNE ON HEXIN FLAG

JNE ON HEXIN FLAG

FE7A	4C4F	573D	00	* INSTRUCTION L			
FE7F	00			* LMESSO	TEXT	'LOW='	
FE80	1602			LOW	EVEN	LOW00	JNE ON HEXIN FLAG
FE82	C3C2				JNE	R2,R15	
FE84	10C8				MOV	MON00C	
FE86	2CE0	F4AA		LOW00	JMP	@MESS03,3	PRINT INDENTATION
FE8A	2CE0	FE7A			XOP	@LMESS0,3	PRINT "LOW="
FE8E	C0BF				XOP	R15,R2	PRINT USER WORKSPACE
FE90	06A0	F5D6			MOV	@HEXOUT0	
FE94	10C0				BL	MON00C	
FE96	1603			* INSTRUCTION W	JMP		
FE98	C802	F3FA		* WP	JNE	WP00	JNE ON HEXIN FLAG
FE9C	10BC				MOV	R2,@2*R13+INT1WP	
FE9E	2CE0	F4AA		WP00	JMP	MON00C	
FEA2	2CE0	FA81			XOP	@MESS03,3	PRINT INDENTATION
FEA6	C0A0	F3FA			XOP	@IMESS1+2,3	PRINT "WP="
FEAA	06A0	F5D6			MOV	@2*R13+INT1WP,R2	
FEAE	10B3				RL	@HEXOUT0	
FE90				* INSTRUCTION O	JMP	MON00C	
FE92				* OUTPUT	MOV	R15,R4	
FE94					MOV	R3,R3	
FE96					JNE	OUTPUT0	
FE98					MOV	R2,R4	MAKE ADDRESS EVEN
FE9A				OUTPUT0	ANDI	R4,FFFFE	
FE9C					MOV	R4,R5	
FE9E					ANDI	R5,FFFF0	
FEC2	0225	0080			AI	R5,#80	PRINT INDENTATION
FEC6	2CE0	F48A		OUTPUT1	XOP	@MESS03+1,3	
FECA	C084				MOV	R4,R2	
FEC8	06A0	F5D6			BL	@HEXOUT0	PRINT ADDRESS
FED0	2CE0	F481		OUTPUT2	XOP	@MESS03+7,3	PRINT "
FED4	C084				MOV	*R4,R2	
FED6	06A0	F5D2			RL	@HEXOUT	
FEDA	8144				C	R4,R5	
FEDC	1304				JEO	OUTPUT3	
FED8	2520	F540			CZC	@MASK16,R4	
FEF2	13F1				JEO	OUTPUT1	
FEF4	10F7				JMP	OUTPUT2	
FEF6	2C81			OUTPUT3	XOP	R1,2	GET CHARACTER
FEF8	0281	2000			CI	R1,1	
FEFC	1694				JNE	MON00C	
FEFE	0225	0040			AI	R5,#40	AN EXTRA 4 LINES
FEF2	10F9				JMP	OUTPUT1	
				* END			
				10000			

** CROSS REFERENCE LISTING **

LABEL NAME	DEFN	VALUE STATEMENT NUMBERS
ATHASH	930	F4C6 8350
C02ATAH	6900	F8BA 7590
COATAB	6870	F8BA 7040
C2ATAH	6890	F8AA 7360
CFIELD	7810	FCCC 7960
CHAR	2550	F638 1040
CHAR00	2570	F63C 2550
CHAR01	2580	F640 2810
CHAR02	2610	F648 2790
CHAR03	2690	F65A 2630
CHAR04	2730	F664 2700
CHAR05	2770	F66E 2750
CODE00	7480	FC70 6880
CODE02	7520	FC7A 6870
CODE04	7620	FC94 6870
CODE040	7700	FCAE 7680
CODE08	7720	FCHO 6880
CODE100	7250	FC32 7190
CODE101	7230	FC2E 7210
CODE2	7290	FC3C 7090
CODE4	7390	FC56 7070
COMMA	950	F4C9 8020
CR	290	000D 5230
DELIM	3760	F87E 4390
DELIM00	3810	F88C 3770
DISASM	6930	F8DA 6740
DISASM1	7060	F8FC 7010
DISASM2	7270	FC3A 6910
DOT	3050	F7DC 3160
DSIX	7860	FCD8 6890
FERRI	5140	F9E0 4980
ERROR	3840	F892 5140
FIND	9130	FE28 1030
FIND00	9150	FE2C 9220
FIND01	9210	FE3B 9410
FIND02	9260	FE46 9200
FIND03	9400	FE6E 9340
FMESS00	9110	FE1F 9240
GETRC	4540	F942 5110
GETRC00	4580	F950 4660
GETRC01	4670	F966 4620
GETRC02	4680	F96A 4710
GETRC03	4730	F976 4690
GU	9460	FE76 1030
HASH	940	F4C7 8030
HEXIN	1900	F5AE 9330
HEXINO	1930	F5B4 2010
HEXIN1	1960	F58C 2030
HEXIN2	2020	F5CA 1970
	7940	7870 7790 7440 3810
	900	870
	4300	3960 3880
	6290	
	4720	4240 4210 4190
	3790	3720 3430 3300
	9160	
	4890	4840 4430 4370 4320 4290 4030
	4600	
	7250	4990 4460 4100
	8810	8750 8700 5360
	5000	4470 4110 2300 690

HEXOUT	2100	F5D2	9880	9310	6980	6580	6180	5340	2490												
HEXOUT0	2110	F5D6	9850	9700	9590	9290	8370	8050	7260	6470	6330	6240	6210	5910	2470						
HEXOUT1	2140	F5DE	2200																		
HEXOUTX	2120	F5D8	1220																		
HEXTAB	990	F4D2	2170	1960																	
IITAB	5770	FA76	5840																		
IMESS0	5780	FA7C	5920																		
IMESS1	5790	FA7F	9680	6450	6190																
IMESS2	5800	FA85	6310																		
IMESS3	5810	FA87	6220																		
IMM	8030	FD00	6910	6900																	
IMMR	8000	FCF6	6900																		
INIT0	460	F40C	490																		
INIT1	530	F422	580																		
INIT2	640	F440	1230																		
INITIAL	420	F400	840																		
INST00	3110	F7E2	3850	3730	3090																
INST01	3180	F7F8	3270																		
INST02	3280	F812	3230	3210																	
INST03	3360	F822	3420																		
INST04	3400	F82C	3370																		
INST05	3470	F838	3390																		
INST06	3490	F83E	3520																		
INST07	3530	F846	3500																		
INST08	3640	F862	4500	4260	4120	4080	3930														
INST09	3670	F86A	3700																		
INST10	3740	F87A	3800	3310																	
INSTANT	3090	F7DE	1040																		
INT1	5830	FA8E	840																		
INT1AA	5870	FA96	5770																		
INT1HR	5900	FAA0	5880																		
INTICC	6040	FAC0	5940	5770																	
INTIDD	6070	FACA	6100																		
INTIEE	6150	FAD8	6080																		
INTIEF	6250	Fafa	6340	5940																	
INTIGG	6280	F802	5770																		
INTIMP	320	F3E0	9690	9650	6380	5670	5660	5630	5490	5270	840	610									
INTAR	1010	F502	710																		
KSIX	7930	FCEA	6890																		
LF	300	000A	5230	900	870																
LMESS0	9510	FF7A	9570																		
LOW	9530	FF80	1040																		
LOW00	9560	FE86	9530																		
MASK16	1070	F540	9910	5690																	
MASK3	1090	F544	6620																		
MASK32	1060	F53E	2780																		
MASKR	1080	F542	5870																		
MESS00	860	F488	670																		
MESS01	870	F49D	680	660																	
MESS02	890	F4A6	5960	3840	750																
MESS03	900	F4AA	9860	9830	9670	9560	9320	9260	9230	8840	8600	8550	6930	6440	5890	5350	3280	2500			
MESS04	920	F4B3	1210	2450	2100																

R15	270	000F	9750	9580	9540	9210	9130	6680	6320	6230	5710	5690	610	590	9280	9270	9140	8830
R2	140	0002	9870	9840	9780	9690	9650	9580	9540	9470	9390	9370	9360	9300	9280	9270	9140	8830
			8790	8770	8720	8360	8040	7240	7230	7220	7200	7170	7160	6970	6940	6710	6570	6460
			6420	6320	6230	6200	6170	6090	6040	5900	5660	5380	5330	5050	5030	4740	4730	4700
			4680	4670	4650	4570	4480	4250	4230	4220	4200	4180	4170	4160	4150	3280	3260	3250
R3	150	0003	3170	3100	2560	2480	2460	2330	2270	2150	2140	2000	1990	1910	1200	540	520	1980
			9760	6690	6400	5640	3530	3490	3480	2800	2600	2570	2210	2190	2130	2110	2040	1980
			1920	790														
R4	160	0004	9910	9890	9870	9840	9800	9790	9780	9750	6570	6460	6430	6420	6390	6380	5630	5600
			5550	5500	5490	5480	3600	3580	3570	3540	3510	3470	2640	2580	2020	2000	1960	1950
			800	780	770	730	710	700										
R5	170	0005	9970	9890	9820	9810	9800	8240	8220	8080	8000	7950	7880	7820	7810	7800	7770	7720
			7670	7620	7520	7290	7180	7160	7110	7020	6990	6970	6960	6620	6600	6590	6530	6480
			5380	5330	5280	5270	4680	4560										
R6	180	0006	9030	9010	9000	8990	8970	8930	8900	8830	8380	8280	8270	8260	8250	8240	8220	8160
			8150	8130	8120	8080	8000	7890	7880	7770	7590	7580	7550	7540	7530	7520	7400	7390
			7360	7350	7320	7310	7300	7290	7080	7060	7040	7030	7020	7000	6990	6640	4550	4540
R7	190	0007	9300	9190	9180	9170	9150	9140	9040	9020	9010	9000	8970	8940	8790	8490	8330	8290
			8270	4380	4310	4250	4130	4070	4000	3980	3970	3920	3890	3580	3560	3530	3490	3410
			3400	3380	3360	3350												
R8	200	0008	9040	8990	8940	8930	8900	8770	8720	8590	8570	5420	5410	5310	5290	5030	4480	3360
			3140	3130														
R9	210	0009	9390	9360	9270	9210	9180	9150	9130	8580	8190	8180	8150	7830	7820	6560	6550	6540
			6530	6510	6500	6000	5930	5870	5050	3380	3140	1690	1170	630				
REG	8120	FD14	8480	8420	8310	8090	8010	7900	7780									
REG00	8190	FD2C	8170															
REG10	6380	FB16	1030															
REG15	6430	FR22	6410															
REG20	6500	FR38	6630															
REG25	6520	FB3E	6640															
RESTAR	840	F474	640	450														
RR	970	F4D0	8140	5100	4880	4420	4360	4280	4070									
SETBP	5270	FA02	1040															
SETHP00	5300	FA0E	5430															
SETHP01	5390	FA2C	5370															
SETHP02	5440	FA3C	5400															
SETUP	5620	FA56	5560	5510														
SETUP00	5670	FA64	5650															
SETUP01	5720	FA72	5700															
SIX	4790	F97C	4060	3950	3900	3870												
SIX00	4860	F98E	4830															
SIX02	4960	F9AE	4920															
SIX03	4970	F9R0	4870															
SIX04	5050	F9C6	5020															
SIX05	5060	F9C8	5040															
SIX06	5150	F9E4	5130															
SIX07	5170	F9E8	5090															
SIXBIT	8260	FD38	8230															
SIXBIT0	8330	FD4C	8300															
SIXBIT2	8460	FD74	8340															
SIXBIT3	8530	FD8A	8500	8450	8390	8320												
SIXBITD	8240	FD34	7450															

SIXBITS	8220	F030	7930	7860	7690	7430
SMFSS0	5230	F9FE	6150	5300		
SMFSS1	5250	F9FA	6160	5320		
STFP	5600	FA52	1040			
SUBTAN	1030	F516	780			
T1	3870	F898	3040			
T100	3910	F8A6	4040			
T101	3920	F8A8	4440	4340		
T2	3950	F8AC	3040			
T200	4020	F8C2	3990			
T201	4030	F8C6	4010			
T3	4060	F8CC	3040			
T4	4100	F8D4	3040			
T400	4230	F8FA	4140			
T5	4280	F904	3040			
T6	4360	F91A	3040			
T7	4420	F92A	3040			
T8	4460	F934	4400	3040		
T9	3630	F860	3040			
TRACE	5480	FA40	1040			
TYBASE	3030	F7H8	3580			
TYPE1	2880	F67A	7410	3350	3000	
TYPE2	2900	F6AA	7330	3000		
TYPE3	2910	F6CA	7650	3000		
TYPE4	2930	F70A	7140	3000		
TYPE5	2950	F74A	7750	3000		
TYPE6	2960	F75A	7560	3010		
TYPE7	2970	F76E	3010			
TYPE8	2980	F776	3010			
TYPE9	2990	F77E	7480	3010		
TYSHIFT	3020	F7AE	3540			
TYSUB	3040	F7CA	3600			
TYTAB	3000	F79A	3480	3410	3010	
WORKSP	340	F3A0	6250	1190	840	620 430
WP	9640	FE96	1030			
WP00	9670	FE9F	9640			
XCLUTE	5550	FA4C	1040			
XOP0	1150	F546	850			
XOP0AA	1240	F562	1180			
XOP0XXX	1190	F54E	1700			
XOP1	1320	F568	850			
XOP2	1400	F56E	850			
XOP2AA	1420	F574	1430			
XOP3	1530	F580	850			
XOP3AA	1550	F584	1590	1560		
XOP4	1660	F590	850			
XOP4AA	1680	F594	1730			
XOP5	1800	F5A2	850			
XOP5AA	1810	F5A4	1840			
XOPTAN	850	F47C	510			
XOPWP	330	F3C0	1200	640	630	520
NOCHARS#	230	NOCHARS#	390	ALPHA#	1.69	

SYMBOL TABLE 5/15 & FULL