

MEM	BS1	BS2	BS3	I/O TRANSFER (O1)	AUMS/L (O2)	INST/DATA* (O3)	INT ACK (O4)	HOLDA (O5)
0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0
0	0	1	0	0	0	1	0	0
0	0	1	1	0	0	1	0	0
0	1	0	0	0	0	0	0	0
0	1	0	1	0	0	0	1	0
0	1	1	0	0	0	0	0	0
0	1	1	1	0	0	0	0	0
1	0	0	0	0	1	0	0	0
1	0	0	1	0	1	0	0	0
1	0	1	0	0	0	0	0	0
1	0	1	1	1	0	0	0	0
1	1	0	0	0	0	0	0	0
1	1	0	1	0	0	0	0	0
1	1	1	0	0	0	0	0	0
1	1	1	1	0	0	0	0	0
1	1	1	1	0	0	0	0	1

RESET (06)

IAQ (07)

MID (08)

0	0	0
0	0	0
0	0	0
0	1	0
0	0	0
0	0	0
0	0	0
0	0	0
0	0	0
0	0	0
0	0	0
1	0	0
0	0	0
0	0	0
0	0	0
0	0	1
0	0	0